

VaST Inaugural Technical Advisory Board (TAB) Meeting

On Sunday, 12 June 2005, VaST held its inaugural Technical Advisory Board meeting in association with DAC in Anaheim, California. A group of experts gathered to present and discuss a number of hairy and significant issues arising in the multi-technology discipline of Embedded Electronic Systems Engineering.

The TAB experts included:

- Mr. Frank Winters, Delphi Electronics and Safety, Kokomo, IN, USA
- Mr. Carsten Mielenze, Infineon Mobility, Munich
- Professor Wayne Wolf, Princeton University, NJ, USA
- Mr. Chuck Pilkington, ST Microelectronics, Ottawa, CA
- Mr. Jon Fields, Agere, NJ, USA
- Dr. Rob Bedichek, VaST, Sunnyvale, CA, USA
- Dr. Graham Hellestrand, VaST, Sunnyvale, CA, USA

The presentation and discussion issues included:

- i. The effect of Virtual System Prototyping on the Systems Engineering process.
- ii. Executable System Specifications: What are they? Can they be mapped onto physical realizations?
- iii. How the Systems Engineering process can be driven empirically to effect the:
Optimization of Executable Specifications leading to optimal physical systems
The use of mappings (such as, specifications → concurrent systems, s/ware → h/ware) in the optimization process.
- iv. Standards for Transaction Level interfacing and interconnection.

The presentations stimulated lively and interesting discussions at the conclusion of each Session. The input to VaST technology thinking and decision making was extremely valuable and helped in (a) broadening its reach by enabling latent aspects of its systems level technology, and (b) depth of coverage into adjacent technology and market areas.

An overview of the TAB, including PDFs of the presentations can be found in the links below:

Technical Advisory Board Meeting – Summary

[http://www.vastsystems.com/docs/VaSTTABSummary_20051226.pdf]

Presentation 1: Virtual Prototyping - Mr. Frank Winters – Delphi

[<http://vastsystems.com/docs/DesignProcessChangesEnablingRapidDevelopment.pdf>]

Presentation 2: Virtual Prototyping in Mobile Systems - Mr. Carsten Mielenz – Infineon

[<http://vastsystems.com/docs/VirtualPrototyping1.pdf>]

Presentation 3: The MultiFlex Approach to System Specification & Mapping – Mr. Chuck Pilkington

[<http://vastsystems.com/docs/The%20MultiFlex%20Approch%20to%20System.pdf>]

Presentation 4: Cool Architectures – Mr. Wayne Wolf – Princeton

[http://vastsystems.com/docs/Cool%20Multiprocessors_Distribute.pdf]

Presentation 5: The Need for Modeling Standards

[<http://vastsystems.com/docs/The%20Need%20for%20Modeling%20Standards06.12.2005.pdf>]

Presentation 6: Empirical Systems Architecture – Mr. Graham Hellestrand – Vast Systems Technology [<http://vastsystems.com/docs/EmpiricalSystemsArchitecture20050722Pub.pdf>]